Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.	Serial No.
	1464-CA	10/682,552
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s)	
(Use several sheets if necessary)	John L. Melanson	
	Filing Date	Group
	October 9, 2003	~~2121~~ 2811

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
MBJ	AA	2003/0163270	08/28/2003	Opitz et al.	702	86	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						☐	☐
	AM						☐	☐
	AN						☐	☐
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AR	
	AS	
	AT	

Examiner Jean Bruner Jean Bruner Date Considered 8/4/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
JBJ	AA	6,331,833	12/0001	Naviasky et al.	341	143	
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	AJ						
	AK						

Foreign Patent Documents

		Document	Date	Country	Class	Subclass	Translation Yes	Translation No
	AL						☐	☐
	AM						☐	☐
	AN						☐	☐
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

JBJ	AR	Fujimori, Ichiro, et al., "A 5v Single-Chip Delta-Sigma Audio A/D Converter with 111dB Dynamic-Range," date unknown
JBJ	AS	Moussavi, S. Mohsen, et al., "High-Order Single-Stage Single-Bit Oversampling A/D Converter Stabilized with Local Feedback Loops," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 41, No. 1, January 1994, pp. 19-25
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Examiner Dean Burke/Jean Jeanglaud Date Considered 8/4/05

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